

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/580,198
Filed : May 23, 2006
Applicant : Kensuke NAKAMURA et al.
TC/A.U. : 1794
Examiner : Cathy Fong Fong LAM
Docket No. : 3209-115
Customer No. : 6449
Confirmation No. : 1843

STATEMENT OF SUBSTANCE OF THE INTERVIEW

MAIL STOP: ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Sir:

Applicants respectfully submit a statement of substance of the interview covering two interviews held with the Examiner as follows:

On October 22, 2008, a telephone Interview was conducted between Robert Murray, attorney for Applicants, and Examiner Lam in the above-referenced application. Applicants' representative orally agreed during the interview to the Examiner's Amendment to incorporate claim 20 into claim 18, and to cancel claims 20, and 26-39, and agrees to the substance of the interview. The amendment will also include a change in the dependence of claim 21 to depend from claim 18, instead of cancelled claim 20. A copy of the Examiner's Amendment received by Applicants' representative by facsimile on October 23, 2008 is attached hereto.

On November 6, 2008, a telephone interview was conducted between Applicants' representative, Gus Pappas, and Examiner Lam in the above-identified application.

Applicants' representative agrees with the substance of the interview as detailed in the Interview Summary Record mailed December 5, 2008, namely to include claim 40 to the allowed claims, since claim 40 was inadvertently omitted in earlier mailed Notice of Allowance on October 29, 2008.

It is believed that this response is timely filed within one month from the date of the Interview Summary Record mailed December 5, 2008, which was mailed together with a Supplemental Notice of Allowability on December 5, 2008. Therefore, no fees are due in connection with this response; however, if any fees are deemed due by the Office, the Commissioner is hereby authorized to withdraw said fees from Deposit Account No. 022135.

Respectfully submitted,

By: 
Robert B. Murray
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RBM/GP:kdm

Attachment (copy of Examiner's Amendment)



UNITED STATES PATENT AND TRADEMARK OFFICE

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Fax Cover Sheet

Date: 23 Oct 2008

To: Mr. Gus Pappas	From: Cathy Lam
Application/Control Number: 10/580,198	Art Unit: 1794
Fax No.: 202-783-6031	Phone No.: (571) 272-1538
Voice No.: (202) 783-6040	Return Fax No.: (571) 273-8300
Rec: 10/580,198 or Atty's doc no. 3209-115	CC:
<input type="checkbox"/> Urgent <input checked="" type="checkbox"/> For Review <input type="checkbox"/> For Comment <input type="checkbox"/> For Reply <input type="checkbox"/> Per Your Request	

Comments:

The Examiner's amendment as we discussed earlier.

Number of pages 2 Including this page

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EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Atty: Robert Murray on October 22, 2008.

The application has been amended as follows:

Substitute claim 18 as following:

18. A multi-layered printed wiring board with an inner layer circuit comprising: a primer resin layer, constituted exclusively of a resin, is provided between said inner layer circuit without roughening treatment and an insulating resin layer, with a surface of the inner layer circuit facing the primer resin layer; a plating of tin, nickel or an alloy of these metals is formed on the surface of the inner layer circuit facing the primer resin layer and a silane coupling agent is provided between said primer resin layer and said plating of tin, nickel or an alloy of these metals.

Claims 20, 26-39 have been canceled.

2. The following is an examiner's statement of reasons for allowance: there is no teaching in the prior art directed to a printed wiring board having a primer resin layer, a silane coupling agent and a tin, nickel or their alloys, between a circuit layer and an insulating substrate.